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Complete if Known **Application Number** 09/650,853 Filing Date 08/29/2000 First Named Inventor Mandeep Singh Chadha Art Unit **Examiner Name** Rexford N. Barnie

(Use as many sheets as necessary)

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

Attorney Docket Number M. CHADHA 3-3-1 Sheet

Examiner	Cite	Document Number	Publication Date	F DOCUMENTS Name of Patentee or		/
Initials*	No.1	Number-Kind Code ^{2 (Fanova)}	MM-DD-YYYY	Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		FOREI	IGN PATENT DOCU	MENTS		
Examiner Initiats*	Cite No.	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	Π
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1		09/650,853	Application Number					
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	dha	Mandeep Singh Cha	First Named Inventor					
04	JUN 0 9 20	2643	Art Unit					
05 2500	Tochnology Con	Rexford N. Barnie	Examiner Name					
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Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
RB	,	Floyd M. Gardner "Interpolation in Digital Modems - Part I: Fundamentals" IEEE Transactoins on Communications, Vol. 41, No. 3, March 1993; pp. 501-507	
		Lars Erup, et al. "Interpolation in Digital Modems - Part II: Implementation and Performance" IEEE Transactions on Communications, Vol. 41, No. 6, June 1993; pp. 998-1008	
		James. D. Barnette, et al.; U.S. Patent Application Serial No. 09/652,116, filed 08/29/2000 entitled "Resampler for a bit Pump and Method of Resampling a Signal Associated Therewith"; pp. 1-59; 6 drawing sheets	
		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,854, filed 08/29/2000 entitled "Separation Circuit for an Echo Canceling System and Method of Operating the Same"; pp. 1-67; 6 drawing sheets	
		James D. Barnette; U.S. Patent Application Serial No. 09/650,850, filed 08/29/2000 entitled "An Interpolator, A Resampler Employing the Interpolator and Method of Interpolating a Signal Associated Therewith"; pp.1-78; 5 drawing Sheets	
R.B.		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,851, filed 08/29/2000 entitled "Filter Circuit for a Bit Pump and Method of Configuring the Same" pp. 1-57; 7 drawing sheets	

	Examiner Signature	Re	UrrdNPamie	Date Considered	10/14/04	
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